

## EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	45	((MAP or (maximum adj2 posteriori)) near2 decod\$3) and (alpha or (forward adj2 metrics)) and (((backward or reveres) near2 metrics) or beta) and extrinsic	USPAT; EPO; JPO; DERWENT	OR	OFF	2006/03/08 13:46
L2	2	((MAP or (maximum adj2 posteriori)) near2 decod\$3) and (alpha or (forward adj2 metrics)) and (((backward or reveres) near2 metrics) or beta) and extrinsic and ((2's adj2 complement) or (two's adj1 complement))	USPAT; EPO; JPO; DERWENT	OR	OFF	2006/03/08 14:10
L3	2	((MAP or (maximum adj2 posteriori)) near2 decod\$3) and (alpha or (forward adj2 metrics)) and (((backward or reveres) near2 metrics) or beta) and extrinsic and ((2's adj2 complement) or (two's adj1 complement) or (2s adj1 complement))	USPAT; EPO; JPO; DERWENT	OR	OFF	2006/03/08 13:51
L4	2	((MAP or (maximum adj2 posteriori))) and (alpha or (forward adj2 metrics)) and (((backward or reveres) near2 metrics) or beta) and extrinsic and ((2's adj2 complement) or (two's adj1 complement) or (2s adj1 complement))	USPAT; EPO; JPO; DERWENT	OR	OFF	2006/03/08 13:50
L5	12	extrinsic and ((2's adj2 complement) or (two's adj1 complement) or (2s adj1 complement))	USPAT; EPO; JPO; DERWENT	OR	OFF	2006/03/08 13:51
L6	3	extrinsic and ((2's adj2 complement) or (two's adj1 complement) or (2s adj1 complement)) and (MAP or (maximum adj2 posteriori))	USPAT; EPO; JPO; DERWENT	OR	OFF	2006/03/08 13:52
L8	3	extrinsic and ((2's adj2 complement) or (two's adj1 complement) or (2s adj1 complement) or (twos adj1 complement)) and (MAP or (maximum adj2 posteriori))	USPAT; EPO; JPO; DERWENT	OR	OFF	2006/03/08 13:53

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L11	95	((2's adj2 complement) or (two's adj1 complement) or (2s adj1 complement) or (two\$2 adj1 complement)) and (((MAP or (maximum adj2 posteriori)) near3 (algorithm\$1 or decod\$3)))	USPAT; EPO; JPO; DERWENT	OR	OFF	2006/03/08 13:57
L13	40	((2's adj2 complement) or (two's adj1 complement) or (2s adj1 complement) or (two\$2 adj1 complement)) and ((MAP or (maximum adj2 posteriori)) adj2 (algorithm\$1 or decod\$3))	USPAT; EPO; JPO; DERWENT	OR	OFF	2006/03/08 13:58
L14	127	((2's adj2 complement) or (two's adj1 complement) or (2s adj1 complement) or (two\$2 adj1 complement)) and ((MAP or (maximum adj2 posteriori)) near3 (algorithm\$1 or decod\$3))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/03/08 14:10
L15	3	((MAP or (maximum adj2 posteriori)) near2 decod\$3) and (alpha or (forward adj2 metrics)) and (((backward or reveres) near2 metrics) or beta) and extrinsic and ((2's adj2 complement) or (two\$2 adj1 complement))	USPAT; EPO; JPO; DERWENT	OR	OFF	2006/03/08 14:11
L16	9	((MAP or (maximum adj2 posteriori)) near2 decod\$3) and (alpha or (forward adj2 metrics)) and (((backward or reveres) near2 metrics) or beta) and extrinsic and ((2's adj2 complement) or (two\$2 adj1 complement))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/03/08 14:16
L17	0	wolf-tod.in.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/03/08 14:16
L18	112	gatherer-alan.in.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/03/08 14:18
L19	3	gatherer-alan.in. and l1	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/03/08 14:22

## EAST Search History

L20	0	"maximum a posteriori decoder" and "alpha block" and "beta block" and "extrinsic block"	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/03/08 14:24
L21	9	("maximum a posteriori decoder" or "MAP decoder") and "alpha block" and "beta block" and "extrinsic block"	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/03/08 14:24


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## » Key

IEEE JNL IEEE Journal or Magazine

IEE JNL IEE Journal or Magazine

IEEE CNF IEEE Conference Proceeding

IEE CNF IEE Conference Proceeding

IEEE STD IEEE Standard

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- ☐ 1. **Periodic trajectories in piecewise-linear maps**  
Mitrovski, C.D.; Kocarev, L.M.;  
[Circuits and Systems I: Fundamental Theory and Applications, IEEE Transactions on Circuits and Systems I: Regular Papers, IEEE Transactions on](#)  
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- ☐ 2. **Data width requirements in SISO decoding with module normalization**  
Yufei Wu; Woerner, B.D.; Blankenship, T.K.;  
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- ☐ **7. Pipelined multipliers for reconfigurable hardware**  
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